

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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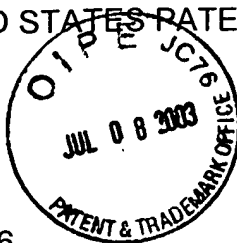
In re the Application of:

Tsuyoshi TOMITA

Application No.: 09/263,766

Filed: March 5, 1999

For: SIGNAL PROCESSING CIRCUIT FOR PREVENTING PSEUDO LOCK OF TIMING
RECOVERY PLL



Group Art Unit: 2631

Examiner: Burd, Kevin M.

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ASSOCIATE POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: July 8, 2003

Sir:

The undersigned, the attorney of record in connection with the above-identified application, hereby grants and appoints as associate attorneys to conduct all business before the United States Patent and Trademark Office relative to said application:

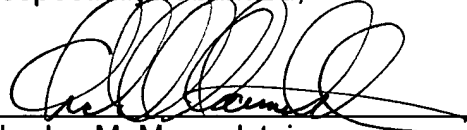
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